

# Medium Voltage Grid Data - Test case FLISR Algorithm

## General

Pbase	Vbase
1 MW	13.8 kV

## Lines

Line	Length [m]	Presence Switch	Line type
S4_O1	2000	yes	1
O1_O2	800	no	1
O1_N2	1000	yes	1
N2_N1	1500	no	1
N1_M4	1500	yes	1
M4_M2	2200	no	1
M2_M3	2200	no	1
M2_M1	800	no	1
M1_L3	800	yes	1
L3_L2	1000	no	1
L2_L1	800	no	1
L1_J2	1600	yes	1
J2_J1	1500	no	1
J1_E4	800	yes	1
E4_E3	1000	no	1
E3_C3	800	yes	1
C3_C1	800	no	1
C1_C2	1200	no	1
C2_F1	800	yes	1
F1_F3	1000	no	1
F1_F2	1000	no	1
F1_M1	1800	yes	1
C1_A2	1000	yes	1
A2_A1	1200	no	1
A1_N2	1000	yes	1
S1_A2	1300	yes	1
A2_A3	800	no	1
A3_B1	2000	yes	1
B1_B2	1600	no	1
B2_E2	1500	yes	1
E2_E1	2000	no	1
E1_E3	1500	no	1
E1_D1	800	yes	1
D1_D2	1800	no	1
S2_D1	800	yes	1
D1_I3	1000	yes	1
I2_I3	800	no	1
I1_I2	1000	no	1
I1_H1	1400	yes	1

H1_H2	1000	no	1
H1_G2	1200	yes	1
G2_B1	800	yes	1
G1_G2	800	no	1
S3_G1	1400	yes	1

### Line Data

Line type	Amax	R_pos_m	X_pos_m	B_pos_m	G_pos_m
1	350	2.350E-04	1.020E-04	1.225E-07	0.000E+00

### Loads

Load	Priority Index	Active_P [W]	Reactive_Q [var]
O1	2	1.07E+05	3.52E+04
O2	3	2.29E+05	7.54E+04
N2	4	1.11E+05	3.64E+04
N1	4	1.32E+05	4.35E+04
M4	1	1.53E+05	5.03E+04
M2	4	4.59E+05	1.51E+05
M3	2	5.12E+05	1.68E+05
M1	3	1.15E+05	3.77E+04
L3	4	4.14E+05	1.36E+05
L2	1	7.83E+05	2.57E+05
L1	3	-2.50E+05	-8.22E+04
J2	1	6.03E+05	1.98E+05
J1	3	6.05E+05	1.99E+05
E4	2	9.30E+05	3.06E+05
E3	4	8.68E+05	2.85E+05
C3	2	1.71E+05	5.60E+04
C1	3	2.65E+05	8.70E+04
C2	1	4.62E+05	1.52E+05
F1	4	2.70E+05	8.88E+04
F2	3	2.21E+05	7.25E+04
F3	4	3.50E+05	1.15E+05
A1	2	3.29E+05	1.08E+05
A2	4	1.53E+05	5.03E+04
A3	1	2.68E+05	8.80E+04
B1	4	3.74E+05	1.23E+05
B2	1	7.02E+05	2.31E+05
E2	3	8.48E+05	2.79E+05
E1	1	8.06E+05	2.65E+05
D1	3	8.03E+05	2.64E+05
D2	2	6.50E+05	2.14E+05
I3	4	8.29E+05	2.72E+05
I2	2	-2.00E+05	-6.57E+04
I1	2	9.04E+05	2.97E+05
H1	1	2.15E+05	7.07E+04
H2	2	4.53E+05	1.49E+05
G2	1	3.38E+05	1.11E+05
G1	3	1.89E+05	6.23E+04